



**Sellweb Ltd**

Inputs		Output of each logic gate.					
A	B	And	Nand	Or	Nor	Ex-or	Ex-nor
0	0	0	1	0	1	0	1
0	1	0	1	1	0	1	0
1	0	0	1	1	0	1	0
1	1	1	0	1	0	0	1